

Routing Algorithms In Networks-on-Chip

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viii Preface a brief introduction on the basic concepts of on-chip networks, routing algorithms for NoC architectures will be presented and discussed at different

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MAFA: adaptive fault-tolerant routing algorithm for networks-on-chip, in: Proceeding of 15th Euromicro Conference on Digital System Design (DSD),

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A Case for Bufferless Routing in On-Chip Networks Thomas Moscibroda Microsoft Research load-balanced adaptive routing algorithm for torus networks. In ISCA, 2003.

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