

# Routing Algorithms In Networks-on-Chip

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## **Fault-aware and Reconfigurable Routing Algorithms -**

IETE JOURNAL OF RESEARCH | VOL 57 | ISSUE 3 | MAY-JUN 2011 215 Fault-aware and Reconfigurable Routing Algorithms for Networks-on-Chip Mojtaba Valinataj, Siamak

## **Routing algorithms in networks-on-chip (eBook, -**

Genre/Form: Electronic books: Additional Physical Format: Print version: Palesi, Maurizio. Routing Algorithms in Networks-on-Chip. Dordrecht : Springer, 2013

## **Routing Algorithms IN Networks ON Chip BY -**

Description: This book provides a single-source reference to routing algorithms for Networks-on-Chip (NoCs), as well as in-depth discussions of advanced solutions

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## **Cluster Based Hierarchical Routing Algorithm for -**

U. Saravanakumar, R. Rangarajan, R. Haripriya, R. Nithya and K. Rajasekar, "Cluster Based Hierarchical Routing Algorithm for Network on Chip," Circuits and Systems

## **Application Specific Routing Algorithms for -**

In this paper, we present a methodology to design highly adaptive routing algorithms for NoC architectures that do not require the use of any VCs.

## **Routing Algorithms in Networks-on-Chip: Maurizio -**

Routing Algorithms in Networks-on-Chip [Maurizio Palesi, Masoud Daneshtalab] on Amazon.com. \*FREE\* shipping on qualifying offers. This book provides a single-source

## **A Case for Bufferless Routing in On-Chip Networks -**

A Case for Bufferless Routing in On-Chip Networks Thomas Moscibroda Microsoft Research load-balanced adaptive routing algorithm for torus networks. In ISCA, 2003.

## **Network on a chip - Wikipedia, the free -**

Network on chip or network the algorithms must be designed in such a way that they offer large parallelism The routing algorithms should be implemented

## **CiteSeerX Network on Chip Routing Algorithms -**

BibTeX @MISC{Rantala06networkon, author = {Ville Rantala and Teijo Lehtonen and Juha Plosila}, title = {Network on Chip Routing Algorithms}, year = {2006}}

### **Routing Algorithms in Networks-on-Chip | Maurizio -**

Provides a comprehensive overview of routing algorithms for Networks-on-Chip and NoC-based, manycore systems ;  
Describes routing algorithms for NoC architectures at

### **Amazon.com: Routing Algorithms in Networks-on-Chip -**

Amazon.com: Routing Algorithms in Networks-on-Chip eBook: Maurizio Palesi, Masoud Daneshtalab: Kindle Store

### **Routing Algorithms in Networks-on-Chip - Springer -**

Routing Algorithms in Networks-on-Chip. Editors: Maurizio Palesi, Learning-Based Routing Algorithms for On-Chip Networks. Masoumeh Ebrahimi, Masoud Daneshtalab.

### **Routing Algorithms in Networks-on-Chip - Maurizio -**

Inbunden, 2013. Pris 1322 kr. K p Routing Algorithms in Networks-on-Chip (9781461482734) av Maurizio Palesi, Masoud Daneshtalab p Bokus.com

### **Deadlock Free Routing Algorithm for Minimizing -**

Deadlock Free Routing Algorithm for Minimizing Data Packet Transmission in Network on Chip:  
10.4018/jertcs.2012010104: Network on chip (NoC) has been proposed as a

### **Network on chip routing algorithms | Juha Plosila -**

Ville Rantala | Teijo Lehtonen | Juha Plosila Network on Chip Routing Algorithms TUCS Technical Report No 779, August 2006 Network on Chip Routing Algorithms Ville

### **Fuzzy-based Adaptive Routing Algorithm for -**

MAFA: adaptive fault-tolerant routing algorithm for networks-on-chip, in: Proceeding of 15th Euromicro Conference on Digital System Design (DSD),

### **Routing algorithms for workload consolidation - -**

Chapter 5 Routing algorithms for workload consolidation Sophisticated routing algorithms can improve the network for adaptive routing in networks-on-chip.

### **Low-overhead Routing Algorithm for 3D Network- -**

Low-overhead Routing Algorithm for 3D Network-on-Chip Akram Ben Ahmed, Abderazek Ben Abdallah The University of Aizu Graduate School of Computers Science and Engineering

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This book provides a single-source reference to routing algorithms for Networks-on-Chip (NoCs), as well as in-depth discussions of advanced solutions applied to

### **Routing Algorithms in Networks-on-Chip - Home - -**

viii Preface a brief introduction on the basic concepts of on-chip networks, routing algorithms for NoC architectures will be presented and discussed at different